



PATENT  
Customer Number 22,852  
Attorney Docket No. 04329.2619

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
)  
Takahito Nakazawa et al. )  
)  
Serial No.: 09/920,628 ) Group Art Unit: 2827  
)  
Filed: August 3, 2001 ) Examiner: Zarneke, David A.  
)  
Allowed: November 20, 2003 ) Confirmation No.: 6946  
)  
For: CHIP PICKUP DEVICE AND )  
METHOD OF MANUFACTURING )  
SEMICONDUCTOR DEVICE )  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**LETTER REQUESTING INITIALED PTO-1449 FORMS**

Applicants filed a Supplemental Information Disclosure Statement on November 5, 2003, and a Second Supplemental Information Disclosure Statement on December 17, 2003. In reviewing our files, we find that we have not received from your office the initialed PTO-1449 forms for these filings, which would serve as acknowledgment of receipt of the Information Disclosure Statements and as an indication that the cited references have been considered by the examiner.

We have enclosed copies of the of the Information Disclosure Statements, the PTO-1449 forms, and the post cards which have been stamped by the U.S. Patent and Trademark Office to indicate receipt of the papers.

FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
www.finnegan.com

The examiner is respectfully requested to return the initialed citations to us.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: January 21, 2004

By: 

Richard V. Burgujian  
Reg. No. 31,744

FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
[www.finnegan.com](http://www.finnegan.com)

RVS/5.17

**PLEASE STAMP TO ACKNOWLEDGE RECEIPT OF THE FOLLOWING:**

In Re Application of: Takahito NAKAZAWA et al.

Application No.: 09/920,628

Group Art Unit: 2827

Filed: August 3, 2001

Examiner: Zarneke, D.

For: CHIP PICKUP DEVICE AND METHOD OF MANUFACTURING  
SEMICONDUCTOR DEVICE

- 
1. Supplemental Information Disclosure Statement Under 37 C.F.R. §1.97(c) (2 pages);
  2. Form PTO-1449 (1 page); and
  3. One (1) cited document; copy of the People's Republic of China Patent Office Action and English-language translation thereof

Dated: November 5, 2003

Docket No.: 04329.2619

RVB/QY/V. Simmons - Mail Drop 848



DRK/11-0-03

RUB/Q41

12

**PLEASE STAMP TO ACKNOWLEDGE RECEIPT OF THE FOLLOWING:**

In Re Application of: Takahito NAKAZAWA et al.

Application No.: 09/920,628

Group Art Unit: 2827

Filed: August 3, 2001

Examiner: Zarneke, D.

For: CHIP PICKUP DEVICE AND METHOD OF MANUFACTURING  
SEMICONDUCTOR DEVICE

- 
1. Second Supplemental Information Disclosure Statement Under 37 C.F.R. §1.97(d) (2 pages);
  2. Form PTO-1449 (1 page);
  3. Three (3) cited documents; copy of the Korean Patent Office Action and English-language translation thereof; and
  4. Check in the amount of \$180.00

Dated: December 17, 2003

Docket No.: 04329.2619

QY/V. Simmons - Mail Drop 848



dmf 12/18/03

10  
12/18/2003